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MEMORY ACCESS INSTRUCTION VECTORIZATION

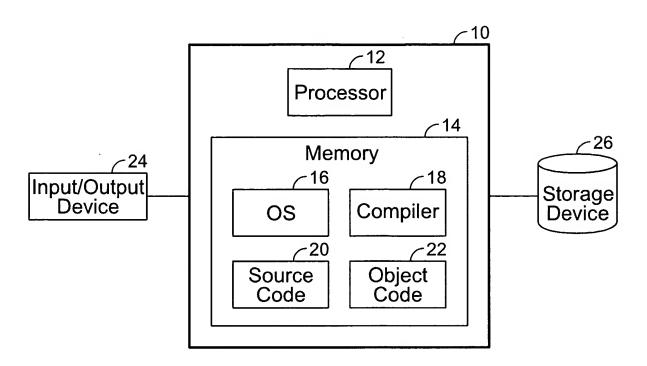


FIG. 1

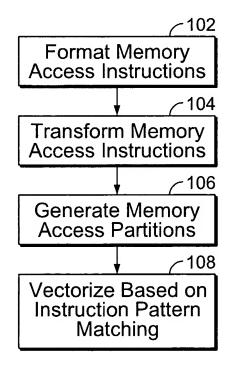


FIG. 2

MEMORY ACCESS INSTRUCTION VECTORIZATION

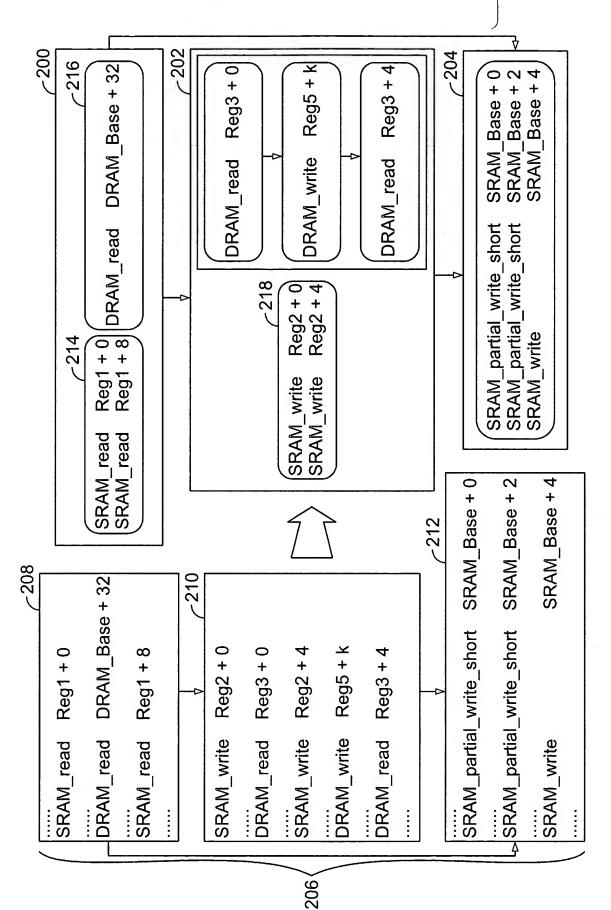


FIG. 3

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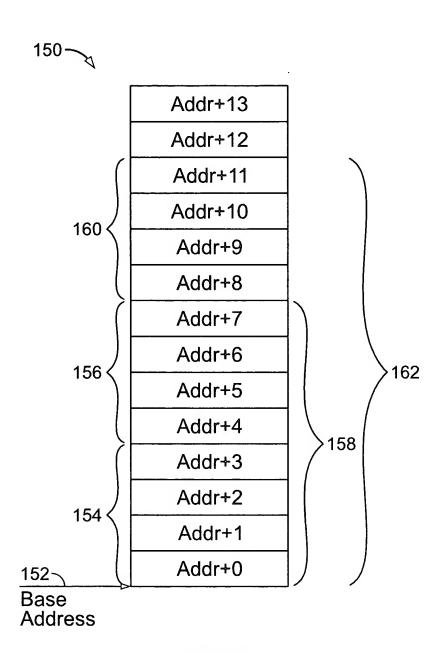


FIG. 4

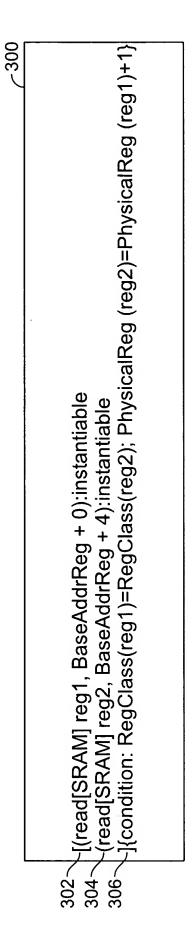


FIG. 5

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MEMORY ACCESS INSTRUCTION VECTORIZATION

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vectorized_SRAM_read_3 Reg1 + 0 DRAM_read DRAM_Base + 32 vectorized_SRAM_write_2 Reg2 + 0 Reg3 + 0 DRAM_read DRAM write Reg5 + k Reg3 + 4 DRAM_read vectorized_SRAM_write_2 SRAM_Base + 0

FIG. 6